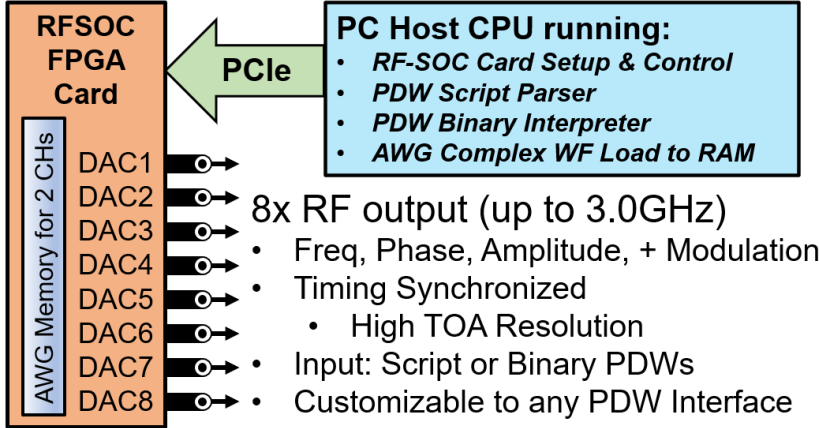


Pulse Descriptor Word to RF Generation System

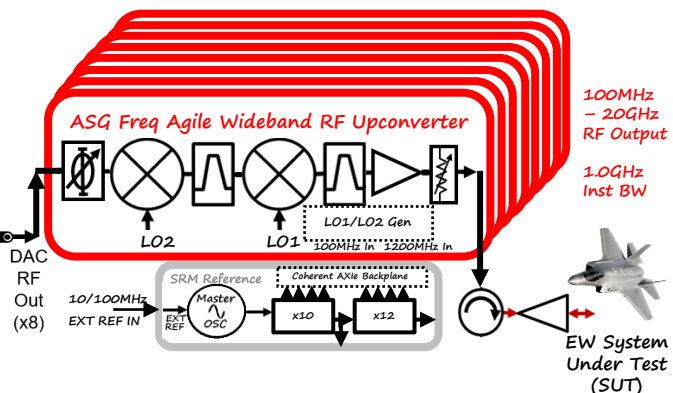
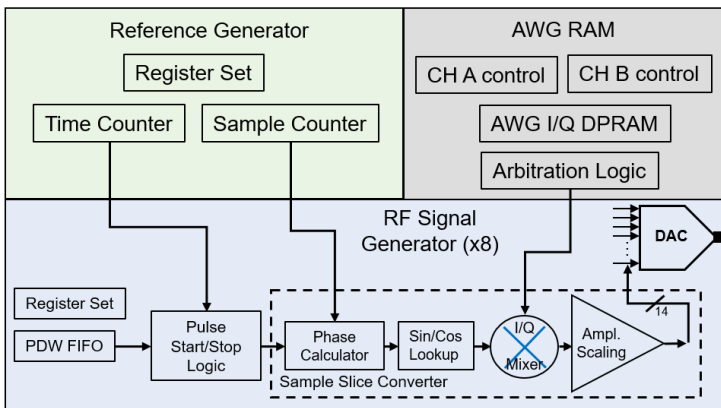
COTS Highly Integrated Open Architecture Multi-Channel RF Threat Emulation for Radar/EW System Development & Test

PDW-to-RF Generation with Inherent AWG Complex Waveform Capability in a single Digital/IF subsystem (no additional "vector box" needed)

- SW/FW Customizable to work with virtually any PDW Interface Spec
- Rehost your present PDW-Driven RF Stimulator Scenarios



- **Multiple Parallel Coherent and Time Synchronous Digital/IF & RF Channels**
- **Digital and IF Core based on the DARPA-funded RF System On a Chip**
 - Incorporates Xilinx® Zynq® UltraScale+™ RFSOC
 - 14-bit 10.0 GSpS D/A Converters
 - 4 or 8 Parallel Digital Channels in a single card
 - PCIe or 3U Open VPX form factors for Digital/IF Subsystem
- **RF/Microwave Core based on Giga-tronics AXle form factor Phase Coherent, Agile, and Wideband (1GHz IBW) RF Upconverters out to 20GHz with Real Time Control of Frequency, Phase, and Amplitude (1Hz, 0.1°, 0.5dB resolutions; 300ns typical/800ns worst case Switching/Settling times)**
 - Ideal for HWIL applications; guaranteed Phase Coherence to any number of RF Channels
 - System Reference Module locks all subsystems to Internal or User-Supplied Reference Clock/Source



COTS and Custom Configurations Available

- Standard Item COTS configs for 2 to 4 RF Channels
- 4U and 7U height 19" width rack mount subsystems
- Custom configs for 5 to 16+ Channels available: Contact Us